

JOB OPPORTUNITIES

About us

Company: ACTIVE SEMI VIETNAM LIMITED

Address: Floor 9th, 3D Building, Duy Tan Street, Dich Vong Hau Ward, Cau Giay district, Hanoi city Website: <u>www.active-semi.com</u>

Active-Semi Vietnam is located in Hanoi city and we are a part of Active-semi Inc., that found in 2004 in Silicon Valley, California, USA by the semiconductor veterans who had worked for many years from TI, Maxim... We are around the world with many sale offices in US, Euro, Japan, Korea, Shanghai, Shenzhen and R&D centers in Vietnam, Shanghai and headquarter in Dallas, USA. We have shipped over 1.5 billion ICs in a broad range of application from power application controller, AC/DC, DC/DC, PMU and you can find us from many end user equipment such as Drone, SSD, Air Conditioner, Ceiling Fan, Tablet, Power Bank, Car Charger, Cellphone Adapter, Wireless Charger...

Active-Semi Vietnam proud of being a pioneer in Integrated Circuit and Analogue IC design in Vietnam since 2004 with many generations of engineer who has grown up and became world class engineers especially in power management IC with many successful commercial IC for our customers. Working with us you have chances to work with the experts with many years of experienced about analog, mixed mode signal IC and working oversea in Shanghai or USA.

We are now opening for:

- ✓ 3 positions of Design Engineer
- ✓ 2 positions of Application Engineer
- ✓ 2 positions of Layout Engineer

JOB DESCRIPTION

Design Engineer

Participate in design the cutting edge, high performance power management products. Develop and maintain circuitry through testing and troubleshooting.

Work closely with applications to ensure commercial success of the developed products through the product definition and the top level system design, silicon verification and release.

Application Engineer

Work as a team with other application engineer and designer to evaluate the new development to ensure high quality product to customer. Design PCB, evaluation plan, lab equipment, reviewing data sheet. Support Product Line and Sale teams to generate sample and EVK.

Support design wins by supporting field/customer over technical matters.

Layout Engineer

Work as a team with other layout engineers and designers, local and/or globally to complete full chip projects. Grow in a multi-discipline and international team of design engineers on innovative standard cell and pixel layout in the most advanced CIS process technologies.

Responsibilities include all aspects of IC mask design from creating block layout to full chip floor plans, planning schedules, hookup, and verification to tape-out



REQUIREMENT

- ✓ Bachelor's Degree in Electrical, Electronic and Telecommunication or related field.
- ✓ Good written and verbal communication skills, good at reading and writing in English is advantage.
- ✓ Strong understanding about the power IC, high power circuit, A/D, D/A...
- ✓ Being able to understand array layout specificity and experience in analog IC layout.
- ✓ Understanding the physical reason being Design Rule Checks, Layout versus Schematics verification and other physical and electrical design rules will be essential.
- ✓ Good at Microsoft Office (Word, Excel, Power point) and presentation.
- ✓ Lab skill.
- ✓ C/C+, VBA programming.

BENEFIT

At Active-Semi, you will work in an international and professional working environment with experts about analogue IC design and attractive offers:

- ✓ *Have attractive, competitive salary and incentives*
- ✓ *Have extra health care insurance*
- ✓ Lunch allowance
- ✓ Have high Social, Health and Unemployment insurances
- ✓ Annual health check
- ✓ Annual company outing
- ✓ Annual leave is added one day for every one year service
- ✓ Birthday gift, wedding gift, new born gift
- ✓ Sport club/activities
- ✓ Other team building activities

CONTACT

Your CV will be sent to: Ms Ellen Pham from 10 Mar to 31 May 2018

Tel: 04 3768720/ ext 169

Mobile: 0977409306

Email: ellenpham@active-semi.com

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